

Fig. 1

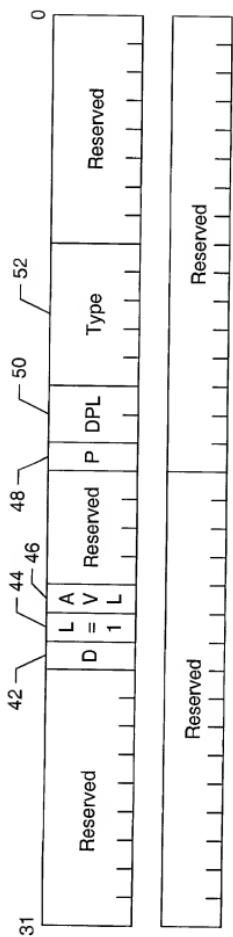


Fig. 2

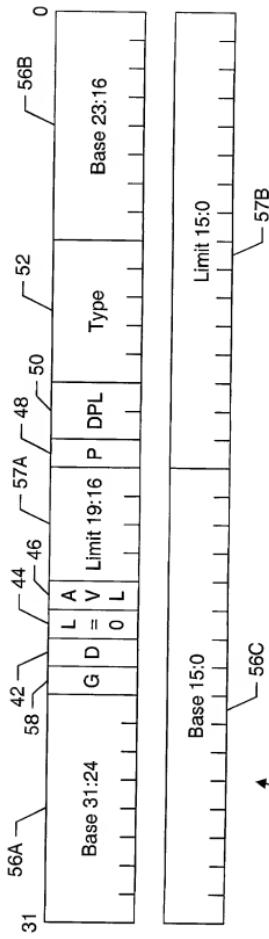


Fig. 3

| <u>LME</u> | <u>CS L Bit</u> | <u>CS D Bit</u> | <u>Operating Mode</u> |
|------------|-----------------|-----------------|---------------------------|
| 0 | x | 0 | 16 Bit Mode |
| 0 | x | 1 | 32 Bit Mode |
| 1 | 0 | 0 | 16 Bit Compatibility Mode |
| 1 | 0 | 1 | 32 Bit Compatibility Mode |
| 1 | 1 | 0 | 32/64 Mode |
| 1 | 1 | 1 | Reserved |

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Fig. 4

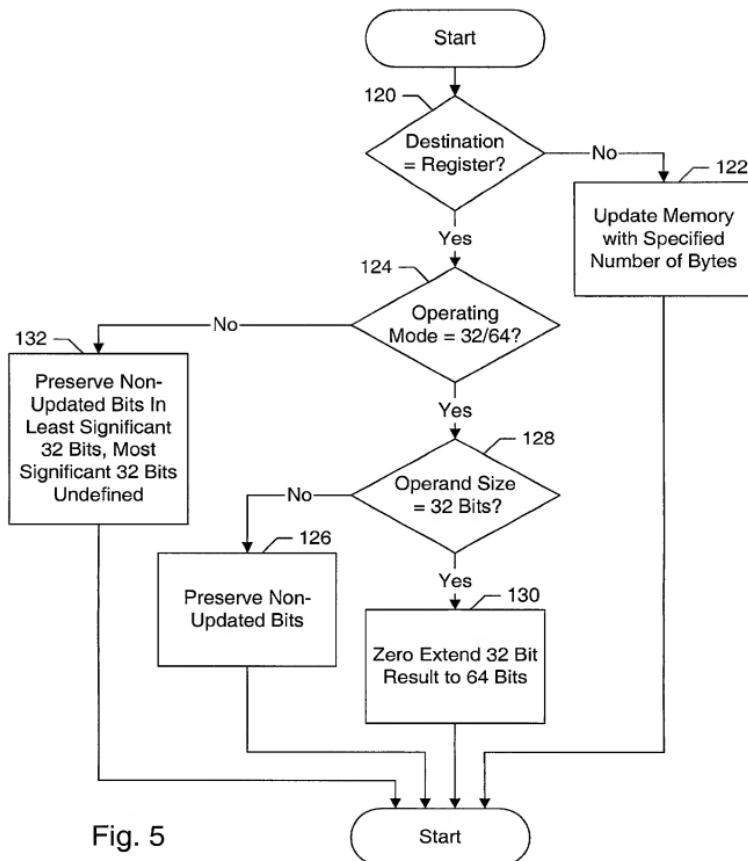


Fig. 5

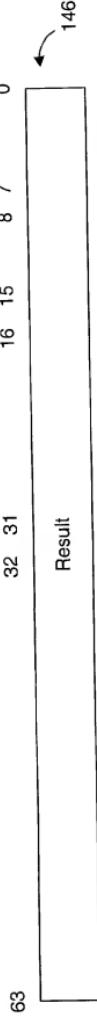
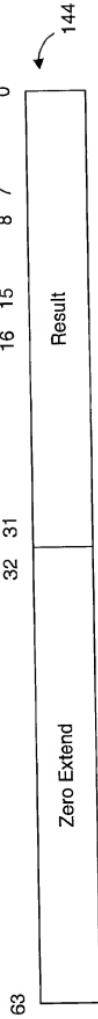
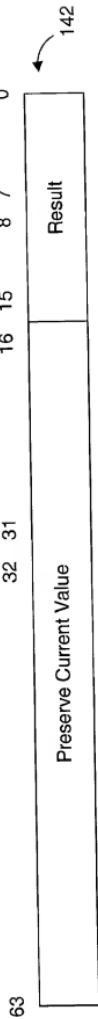
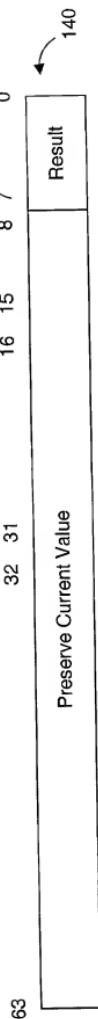


Fig. 6

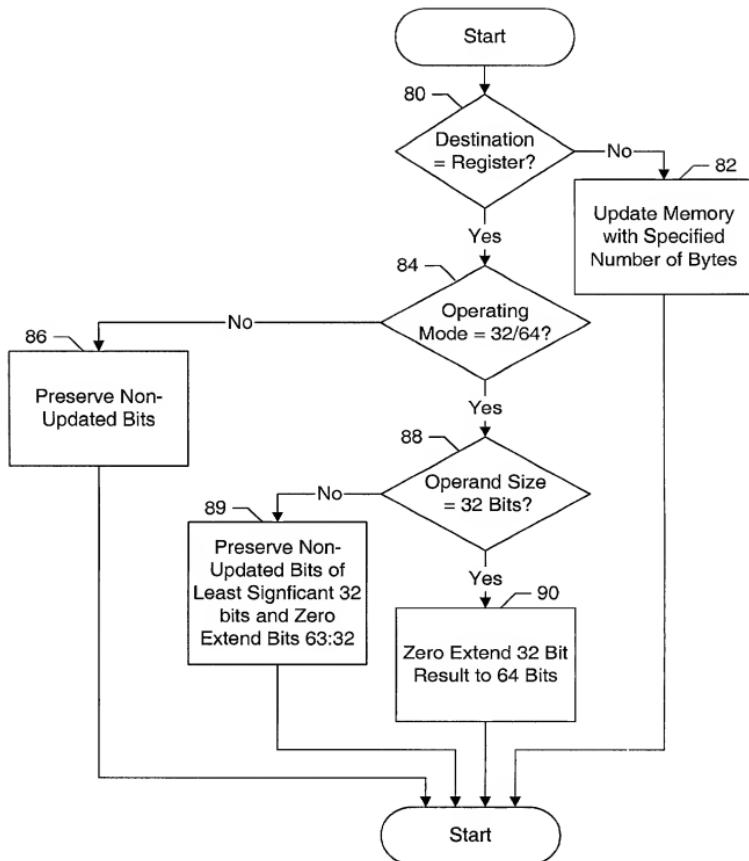


Fig. 7

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| | | | | | | | |
|-------------|------------------------|----|----|----|---|---|--------|
| 63 | 32 | 31 | 16 | 15 | 8 | 7 | 0 |
| Zero Extend | Preserve Current Value | | | | | | Result |

| | | | | | | | |
|-------------|------------------------|----|----|----|---|---|--------|
| 63 | 32 | 31 | 16 | 15 | 8 | 7 | 0 |
| Zero Extend | Preserve Current Value | | | | | | Result |

| | | | | | | | |
|-------------|--------|----|----|----|---|---|-----|
| 63 | 32 | 31 | 16 | 15 | 8 | 7 | 0 |
| Zero Extend | Result | | | | | | 104 |

| | | | | | | | |
|-------------|--------|----|----|----|---|---|-----|
| 63 | 32 | 31 | 16 | 15 | 8 | 7 | 0 |
| Zero Extend | Result | | | | | | 106 |

Fig. 8

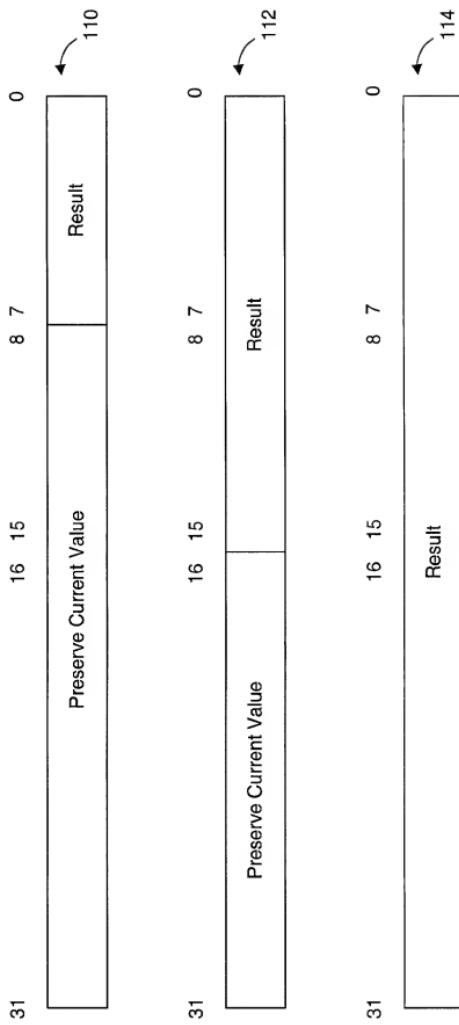


Fig. 9

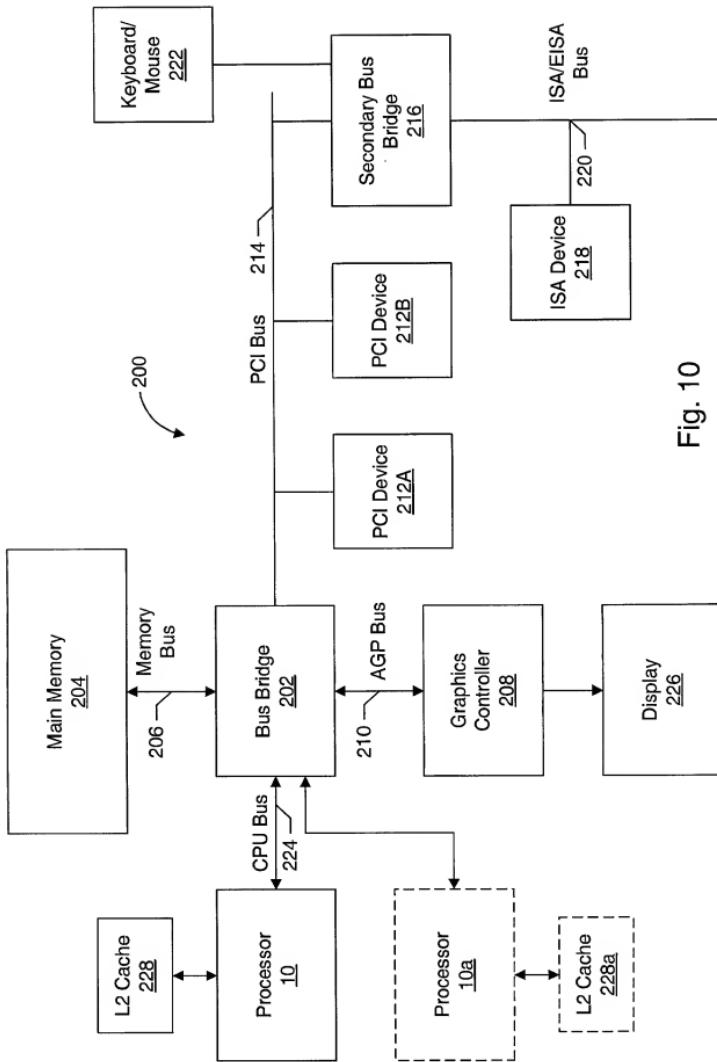


Fig. 10

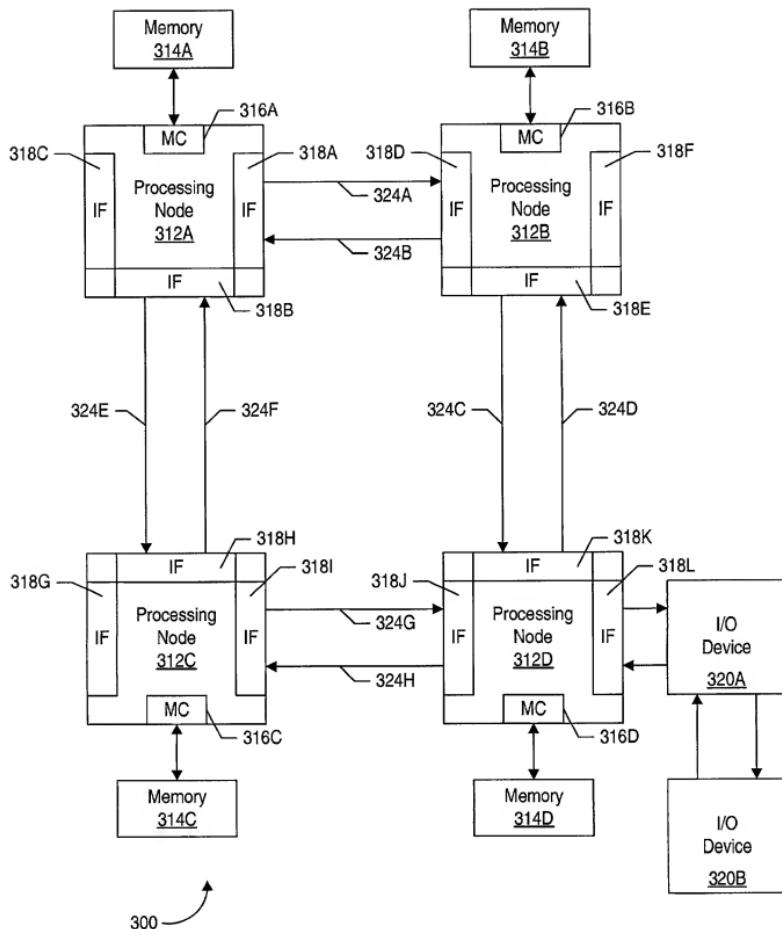


Fig. 11

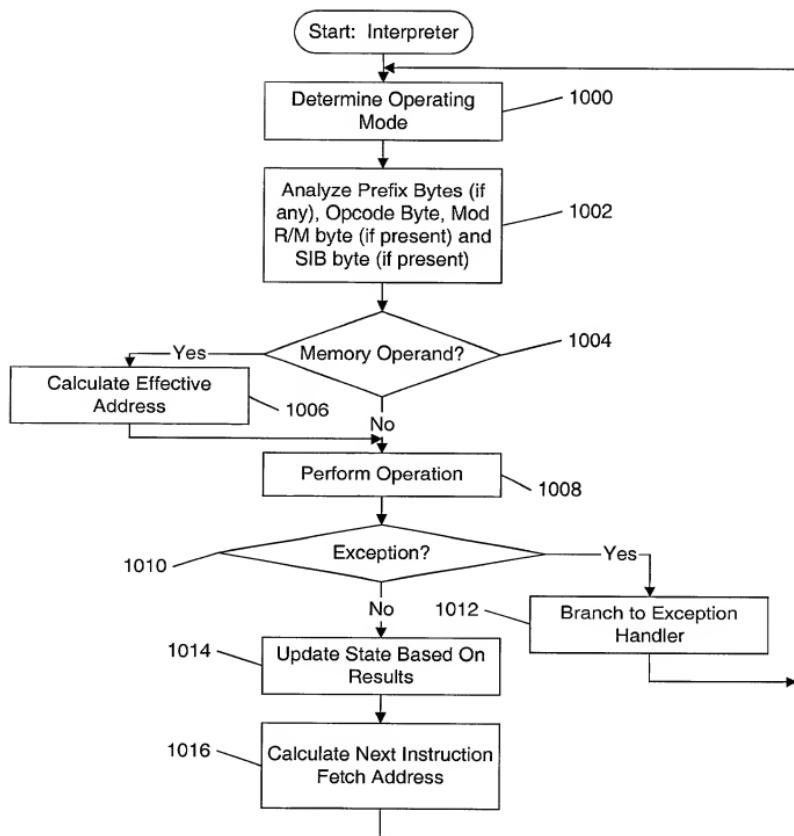


Fig. 12

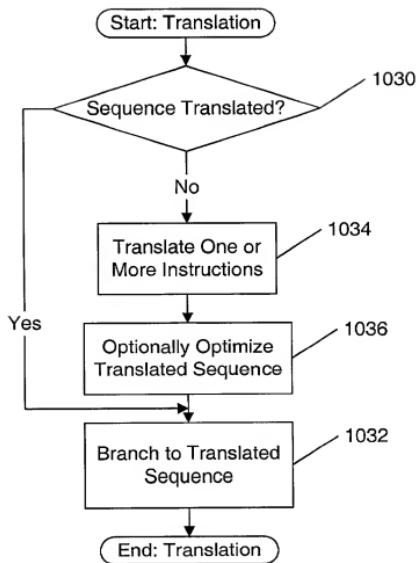


Fig. 13

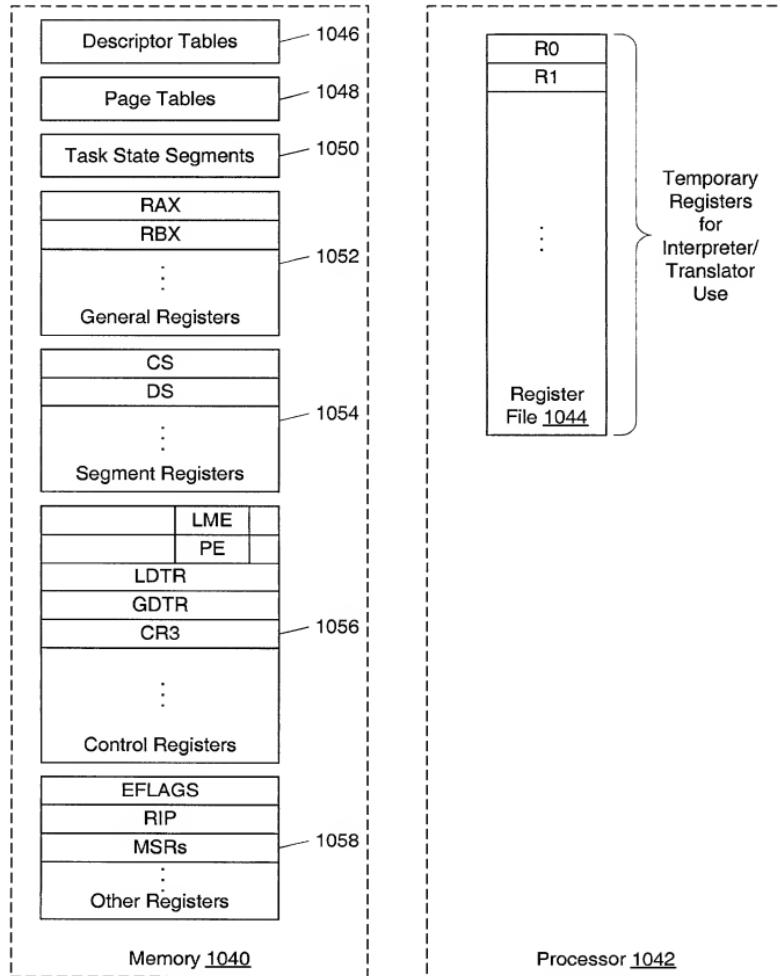


Fig. 14

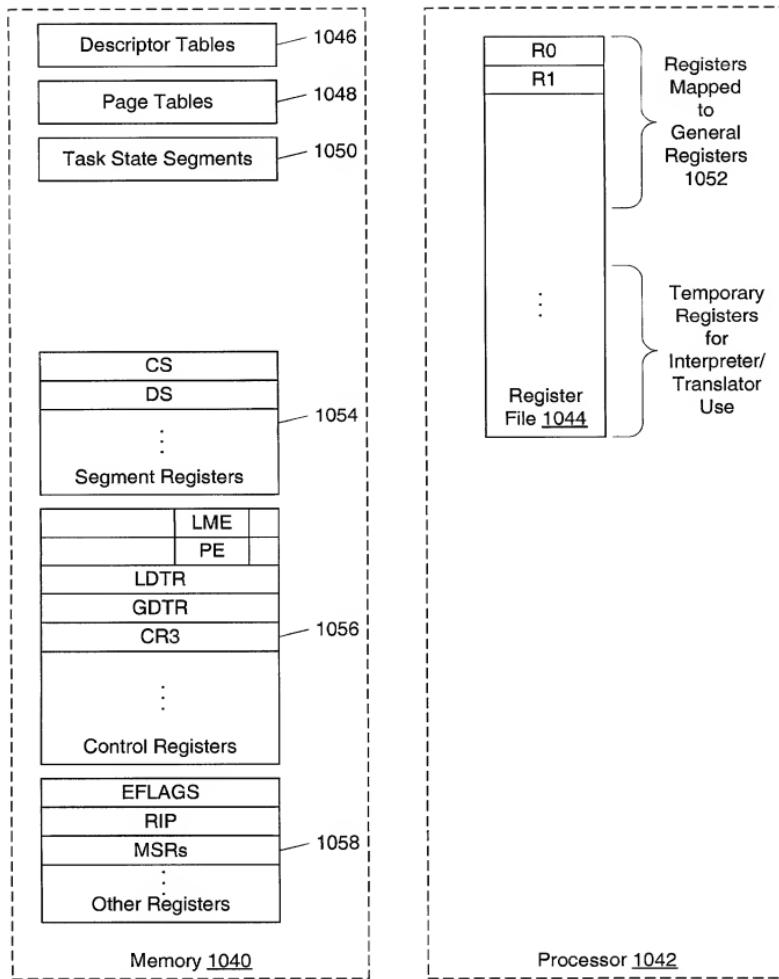


Fig. 15

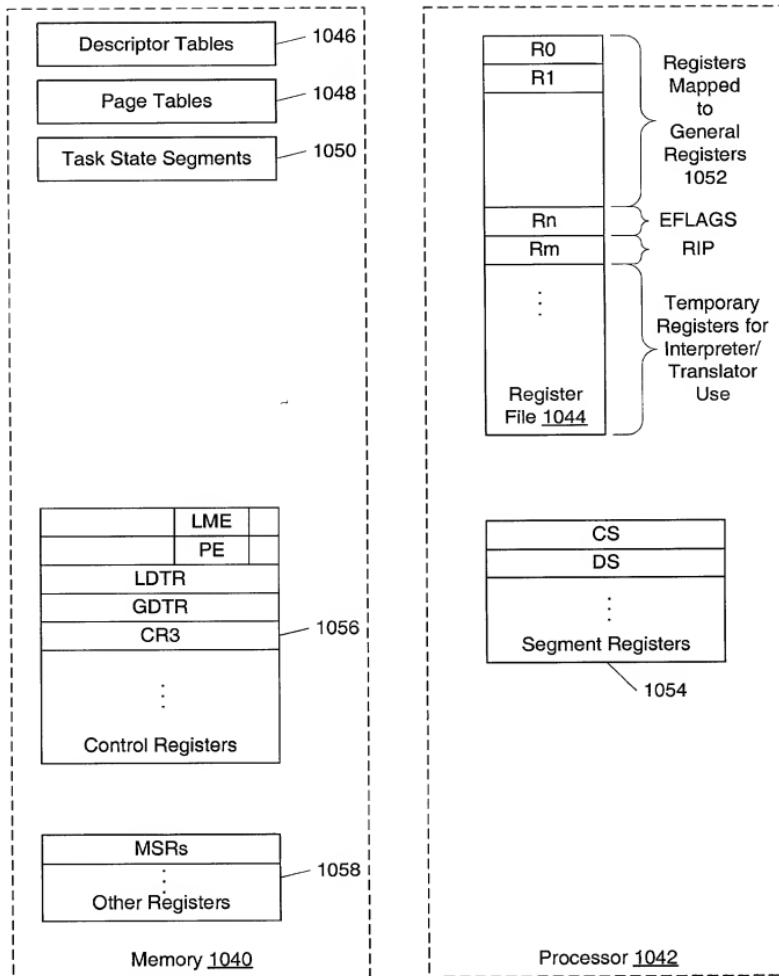


Fig. 16

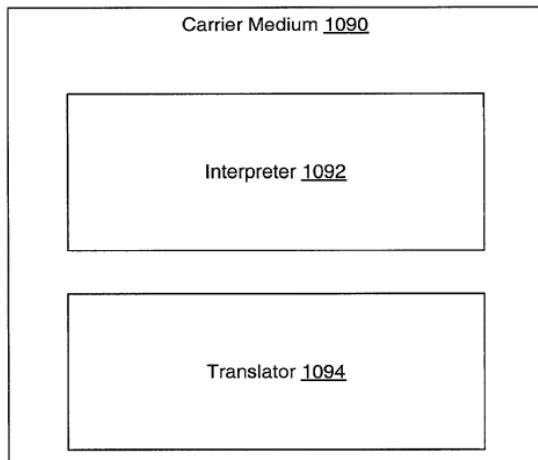


Fig. 17